**Digital Circuits Study Guide**

**Random Chapter 1 shit**

ANSI is 7 bit character set

**Decimal to binary**

1. Divide by two method
   1. Divide # given by 2
      1. If remainder = # then binary dig 1
      2. If remainder = 0 then binary dig 0

**Binary to hex**

Basically split up into 4 bits each, then convert each one to hex.

**Hex to Binary**

**Quick and dirty example of module creation in verilog**

Module\_name

Input a,b,c,d;

Ouput x,y,z;

Wires w1, w2;

//module\_type instance\_name (.in\_module1(a), .in\_module2(b));

//example

nutsacks u1(.input\_1(a), .input\_2(b), .output\_1(w1));

endmodule